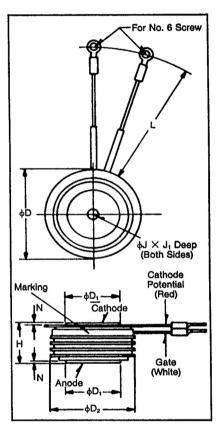


# **TA20**

Powerex, Inc. Hillis Street, Youngwood, Pennsylvania 15697 (412) 925-7272
Powerex Europe, S.A., 428 Ave. G. Durand, BP107, 72003 LeMans, France (43) 72.75.15

Phase Control SCR 1600-1800 Amperes Avg 100-2200 Volts

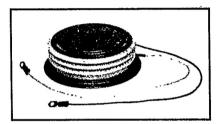


## TA2 Outline Drawing

Dimensions	Inches		Millimeters		
	Min.	Max.	Min.	Max.	
φD	3.910	3.950	99.31	100.33	
φD₁	2.470	2,480	62.74	63.00	
φD <sub>2</sub>	3,440	3.560	87.38	90.42	
Н	1.260	1.300	32.00	33.02	
фJ	.135	.145	3.43	3.68	
Jı	.075	.090	1.91	2.29	
L	11.50	12.50	292.10	317.50	
N	.050	_	1.27		

Creep Distance—1.40 in. mln. (35.56 mm)
Strike Distance—.98 in. mln. (24.89 mm).
(In accordance with NEMA standards.)
Finish—Nickel Plate.
Approx. Weight—2.1 lb. (950 g).

1. Dimension "H" is a clamped dimension.



TA20
Phase Control SCR
1600-1800 Amperes/100-2200 Volts

#### Description

Powerex Silicon Controlled Rectifiers (SCR) are designed for phase control applications. These are all-diffused, Press-Pak (Pow-R-Disc) devices employing the field-proven amplifying (di/namic) gate.

#### Features:

- ☐ Low On-State Voltage
- ☐ High di/dt
- ☐ High dv/dt
- ☐ Hermetic Packaging
- ☐ Excellent Surge and I²t Ratings

#### **Applications:**

- ☐ Power Supplies
- □ Battery Chargers
- ☐ Motor Control
- ☐ Light Dimmers
- ☐ VAR Generators

## **Ordering Information**

Example: Select the complete eight digit part number you desire from the table – i.e. TA200816 is a 800 Volt, 1600 Ampere Phase Control SCR.

	Voltage*		Current		
Туре	Vorm Vram	Code	it (avg)	Code	
TA20	100	01	1600	16	
	200	02	1800	18	
	400	04			
	600	06			
	800	08			
	1000	10			
	1200	12	Ì		
	1400	14			
	1600	16			
	1800	18			
	2000	20			
	2200	22			

<sup>\*</sup> All voltages not available in all current ratings.



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# **Absolute Maximum Ratings**

	Symbol	TA20 16	TA20 18	Units
Maximum Blocking Voltage	V <sub>DRM</sub> , V <sub>RRM</sub>	2200	1800	Volts
RMS On-State Current	I <sub>T(AMS)</sub>	2500	2820	Amperes
Average On-State Current	I <sub>T(av)</sub>	1600	1800	Amperes
Peak One-Cycle Surge (Non-Repetitive) On-State Current (60Hz)®	I <sub>TSM</sub>	29,500	40,000	Amperes
Peak One-Cycle Surge (Non-Repetitive) On-State Current (50Hz)®	I <sub>TSM</sub>	26,900	36,500	Amperes
Critical Rate-of-Rise of On-State Current (Non-Repetitive) © ©	di/dt	400	400	Amperes/µs
Critical Rate-of-Rise of On-State Current (Repetitive)	di/dt	150	150	Amperes/µs
I²t (for Fusing), One Cycle at 60 Hz	l²t	3.63 × 10 <sup>6</sup>	6.67 × 10 <sup>6</sup>	A <sup>2</sup> sec
Peak Gate Power Dissipation	P <sub>GM</sub>	16	16	Watts
Average Gate Power Dissipation	P <sub>G(av)</sub>	3	3	Watts
Storage Temperature	T <sub>STG</sub>	-40 to 150	-40 to 150	*C
Operating Temperature	TJ	-40 to 125	-40 to 125	*C
Mounting Force <sup>®</sup>		9000 to 11,000	9000 to 11,000	lb.
Mounting Force <sup>©</sup>		4100 to 5000	4100 to 5000	kg

# **Electrical and Thermal Characteristics**

	Symbol	Test Conditions	TA20 16	TA20 18	Units
Current—Conducting State Maximums Peak On-State Voltage	V <sub>TM</sub>	i <sub>TM</sub> = 3000A, T <sub>J</sub> = 25°C	1,75	1,45	Volts
			TA2	1	
Voltage—Blocking State Maximums® Forward Leakage, Peak	I <sub>DRM</sub>	$T_J = 125^{\circ}C$ , $V_{DBM} = rated$	100		4
Reverse Leakage, Peak	I <sub>RRM</sub>	$T_J = 125^{\circ}C$ , $V_{BBM} = rated$			mA
Switching Typical Turn-Off Time	tq	I <sub>T</sub> = 250A, T <sub>J</sub> = 125°C, di <sub>R</sub> /dt = 50A/μsec, reapplied dv/dt = 20V/μsec linear to 0.8V <sub>DRM</sub>	250		mA µsec
Typical Turn-On Time®	ton	$I_{TM} = 1000A, V_D = 1500V$	4.0		
Min. Critical dv/dt exponential to VDRM®®	dv/dt	T <sub>J</sub> = 125°C	300		μsec
Thermal  Maximum Thermal Resistance, <sup>©</sup> double sided cooling  Junction to Case	R <sub>ec</sub>	., = 120 0		15	V/μsec •C/Watt
Case to Sink, Lubricated	Recs			07	
Gate—Maximum Parameters Gate Current to Trigger	lat	$T_{J} = 25^{\circ}C, V_{D} = 12V$	200	07	*C/Watt
Gate Voltage to Trigger	Voltage to Til		3.0		mA Valla
Non-Triggering Gate Voltage V <sub>GDM</sub>		$T_J = 25^{\circ}C, V_D = 12V$ $T_J = 125^{\circ}C, rated V_{DBM}$	.15		Volts
Peak Forward Gate Current	Ідтм	13 - 120 O, lated V <sub>DRM</sub>		0	Volts
Peak Reverse Gate Voltage	V <sub>GRM</sub>		<u>4</u> 5		Amperes Volts

① Consult recommended mounting procedures.

② Applies for zero or negative gate blas.

③ Per JEDEC RS-397, 5.2.2.1.

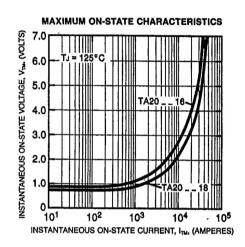
With recommended gate drive.

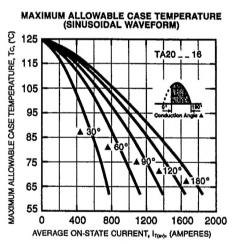
<sup>6</sup> Higher dv/dt ratings available, consult factory.

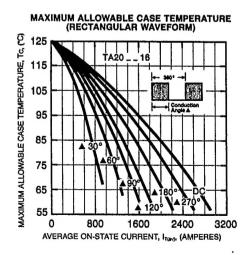
Per JEDEC standard RS-397, 5.2.2.6.

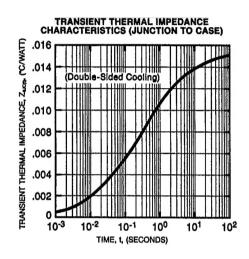
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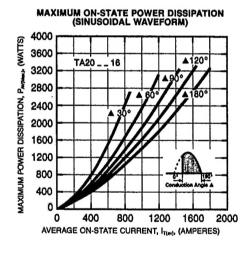
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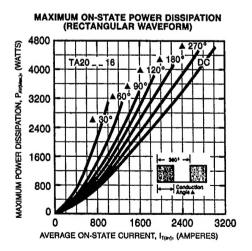














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